Bryan J. Berns

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Objective

Seeking a leadership role in software or electrical engineering.

Education

Bachelor of Science, Electrical Engineering (May 2004)
Additional Major: Computer Science
University of Wisconsin - Madison

- Major GPA: 3.47, Cumulative GPA: 3.11
- Completed two majors in four years while working 30 hours per week
- Emphasis in Electromagnetic Fields & Waves

Work Experience

Bechtel - Knolls Atomic Power Laboratory, Niskayuna, NY
Senior Electrical Engineer (2005-Current)

- Develop and support test platform for Navy I & C test equipment
- Develop Java, .Net, and Oracle application for test platform and team management
- Systems architect for local area network (Windows, Cisco, Solaris, Linux)
- Develop / maintain VxWorks drivers and executive for VME-based I/O system
- Act as expert consultant in community for variety of software engineering areas
- Act as software quality control and configuration management coordinator
- Plan and track fiscal budget of test platform group
- Solicit, prioritize, and distribute incoming work for test platform group
- Act as information systems security officer for electrical laboratories

Avista, Inc (Subsidiary of Esterline, Inc) , Platteville, WI
Software Engineer (2004-2005)

- Developed verification code for two FMS systems which will run on Boeing 737/767
- Trained and advised oncoming team members
- Developed time-saving documentation tools used by a team of engineers

University of Wisconsin - Madison, Computer Aided Engineering, Madison, WI
NovelI Netware / Windows System Administrator & Programmer (2001-2004)

- Trained and advised 20 first/second level support personnel
- Deployed standard computer images for over 400 Dell machines via Ghost
- Programmed multiple applications to aid in network administration
- Configured, deployed, and managed over 300 applications with ZENWorks

ShopKo Stores, Inc. (Corporate Headquarters), Green Bay, WI
Workstation & Network Support Engineer (1999-2000)

- Managed workstation distribution and inventory
- Created, deployed images for 1000+ IBM workstations in 4 corporate buildings
- Provide on-site technical support for three buildings

Related Projects

ECE 554 - Digital Engineering Lab
Team Project: Designed and implemented an explicitly multi-threaded 16-bit CPU aimed at DSP applications. The processor supported interrupts, sub-word arithmetic, exceptions, a VGA interface, a PS/2 interface, and a serial interface. The design was implemented on XESS prototyping board with the Virtex XCV800 FPGA. Personal contributions included programming a versatile assembler, interrupt/exception supporting kernel and a MFC Windows application to transfer data to the processor over a serial connection.

ECE 552 - Microprocessor Design
Team Project: Schematically designed generic 16-bit CPU with a cache implementation at gate level. In comparison to 13 competing teams, the design required the least physical resources and was the fastest in simulation.

Activities & Awards

Certifications

- Auto-qualifier for MENSA (1998)
- Engineer-In-Training, pursuing Professional Engineer registration (2004)
- DoE Security Clearance Level 'L' (2005)
- DoE Security Clearance Level 'Q' (2005)

Awards

- Nominee for Green Bay Youth Philanthropy award (2000)
- Lockheed Martin Spot Award for decisive actions that maintained test program (2005)
- Lockheed Martin Spot Award for development activites on serial card firmware (2006)
- Lockheed Martin Spot Award for technical expertise and customer support (2008)

Extracurricular Activities

- Webmaster for SHPE, Society of Hispanic Professional Engineers (College, 2 years)
- Tutor for variety of undergraduate classes (College, 4 years)

Software Skills

Operating Systems

- MS-DOS / PC-DOS (6 years)
- Windows 9x (6 years)
- Windows NT 4.0 (2 years)
- Windows 2000 (6 years)
- Windows XP (7 years)
- Novell Netware (5 years)
- Solaris / SunOs (5 years)
- Linux (3 years)
- VxWorks (2 year)

Programming Languages / Frameworks

- C / C++ (6 years)
- Java, Java Swing (5 years)
- MFC (3 years)
- Perl (3 years)
- SQL (3 years)
- Assembly -- x86, PPC (2 years)
- HTML (5 years)
- Shell Script -- DOS, CSH (6 years)
- C#, VB.Net (2 years)

Software Packages (IT)

- Active Directory (6 years)
- Novell ZENWorks (5 years)
- Microsoft Office (9 years)
- Visual Studio (6 years)
- Symantec Ghost (6 years)

Software Packages (Engineering)

- Electronics Workbench (2 years)
- PSpice (1 year)
- ModelSim (2 years)
- XILINX ISE (3 years)
- FPGA Express (1 year)

Hardware Skills

- HP Digital / Analog Oscilloscopes (3 years)
- HP Digital Multimeters (4 years)
- HP Logic Analyzers (3 years)
- XILINX FPGAs (3 years)
- HDL Languages: Verilog / VHDL (1 year)
- XESS Prototyping Boards (2 years)
- VME Bus Interface (3 years)
- Cisco Switches (2 years)